

ABSTRACT

A receiver of double conversion system wherein unwanted components included in received signals can be removed without fail and wherein the number of constituent parts has been reduced. The receiver comprises an antenna tuning circuit 10 including a tuning coil 11 and a variable-capacitance diode 13; a high frequency amplification circuit 20 for performing a high frequency amplification of a signal outputted by the antenna tuning circuit 10; two-stage mixing circuits 22, 28 for performing two frequency conversions of an output from the high frequency amplification circuit 20; and a detecting circuit 36 for detecting an output from the latter-stage mixing circuit 28.